

Amendments to the Specification:

Please replace the paragraph beginning at page 9, line 27, with the following redlined paragraph:

Figure 4 illustrates an extractor circuit ~~80-81~~ in which a first diode 82, preferably a Schottkey diode, is coupled between a ground reference potential 84 and the antenna 86, and a second diode 88, preferably a Shottkey diode, is coupled between the antenna 86 and a power-in node 90. A filter capacitor 92 is coupled between the ground reference potential 84 and the power-in node 90. The received radio-frequency signals are rectified through the extractor circuit ~~80-81~~ to obtain operating current for use by the microprocessor, and when available for storage for later use. This particular extractor circuit is preferred because it is easily integrated in a semiconductor circuit.